

FIG. 1

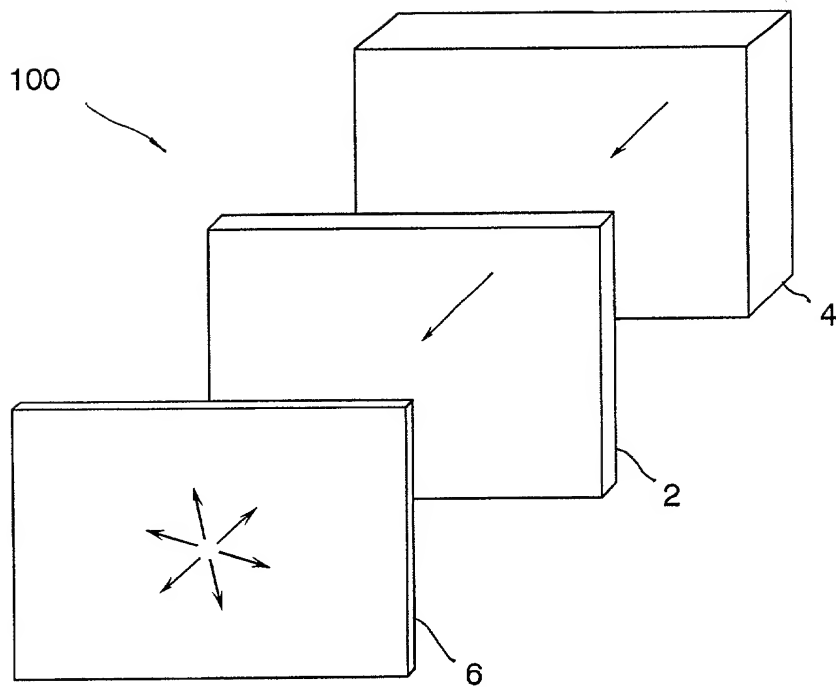


FIG. 2

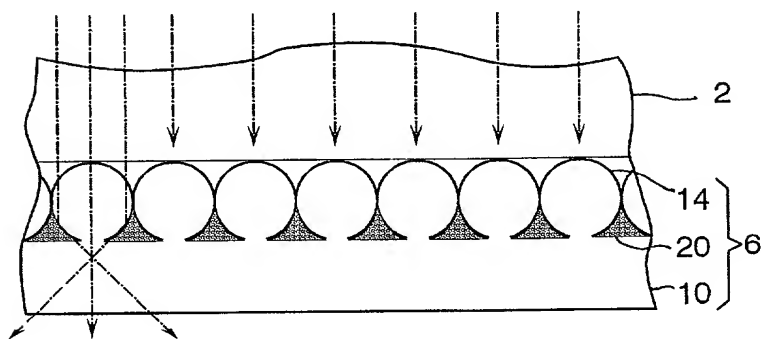
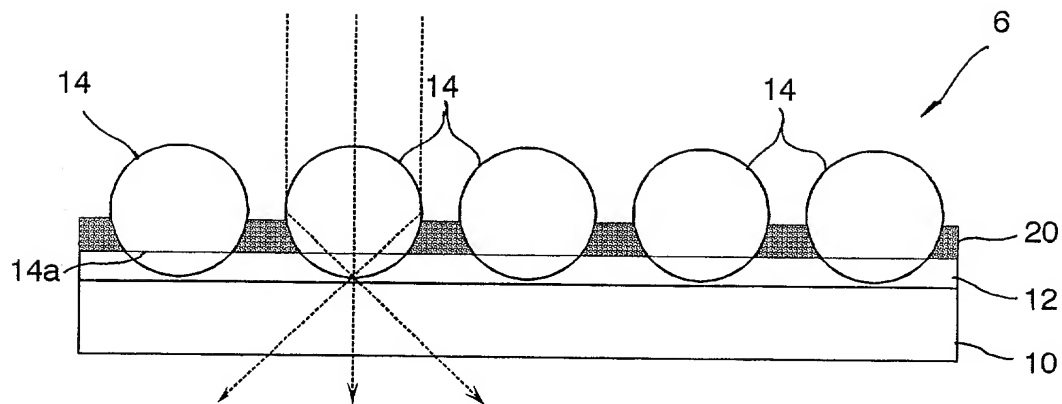
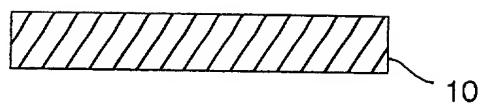


FIG. 3

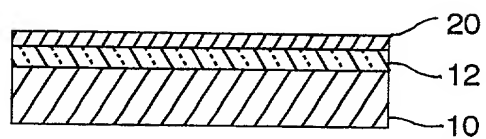


# FIG. 4

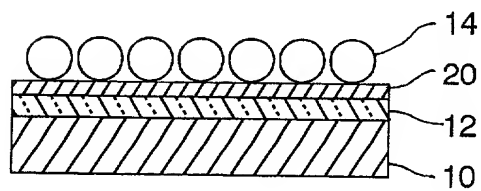
STEP I



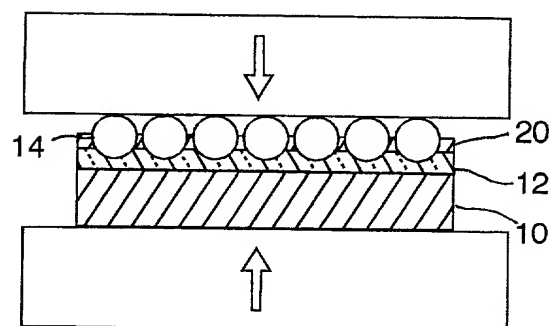
STEP II



STEP III



STEP IV



STEP V

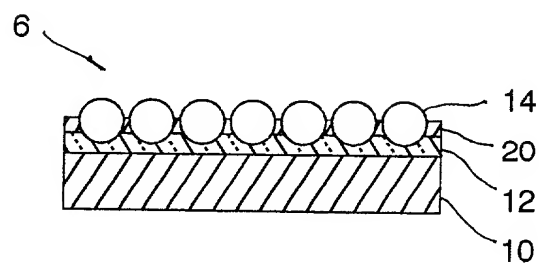


FIG. 5

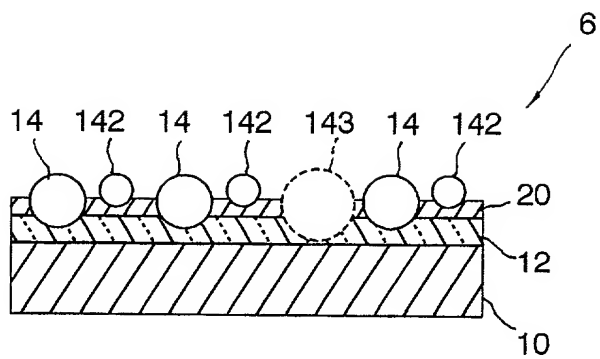


FIG. 5 is a cross-sectional view of a device structure 6. The structure includes a substrate 10, a layer 12, and a layer 20. On top of layer 20, there are several circular features. From left to right, the features are labeled 14, 142, 14, 142, 143, 14, and 142. Feature 143 is shown with a dashed outline, indicating it is a virtual or reference feature. The entire assembly is labeled 6 with a curved arrow pointing to it.

FIG. 6

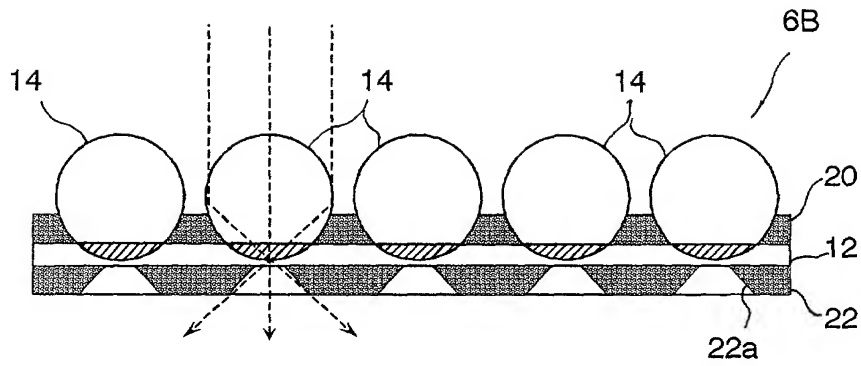


FIG. 7

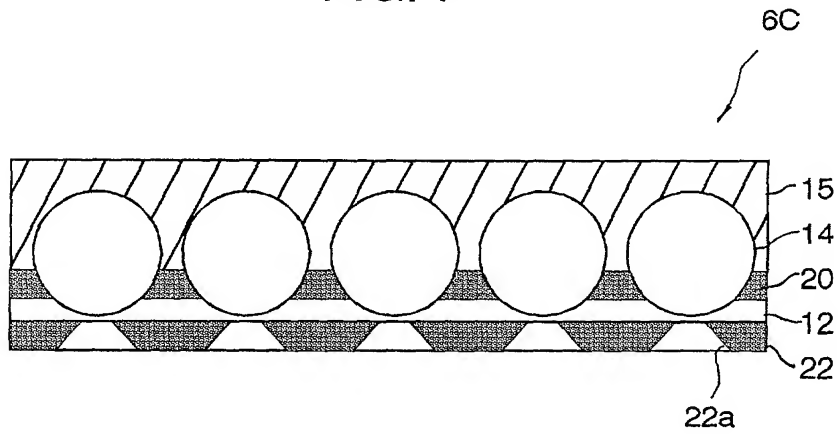


FIG. 8

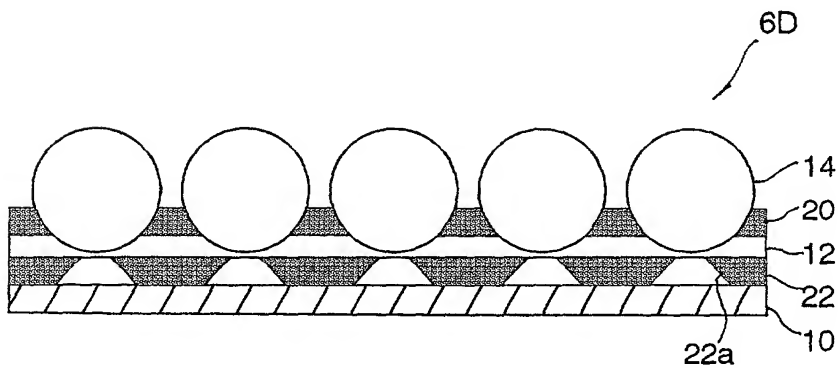
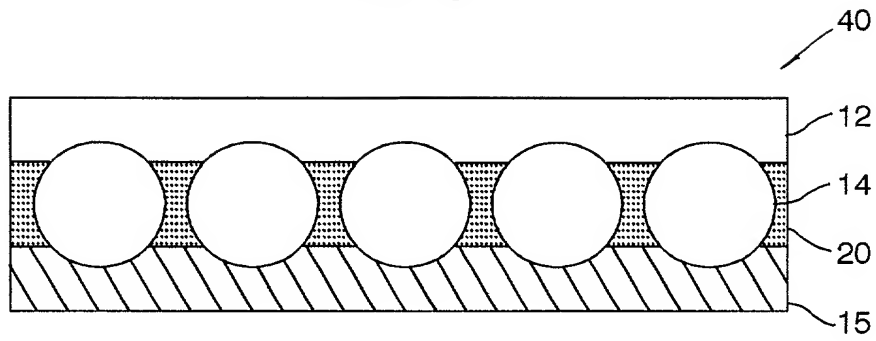
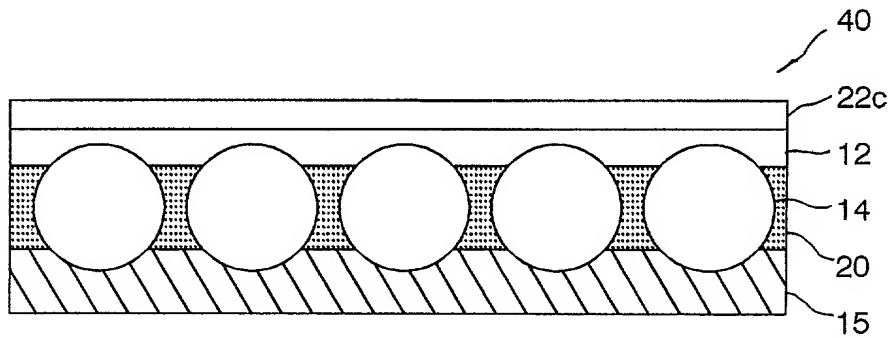


FIG. 9

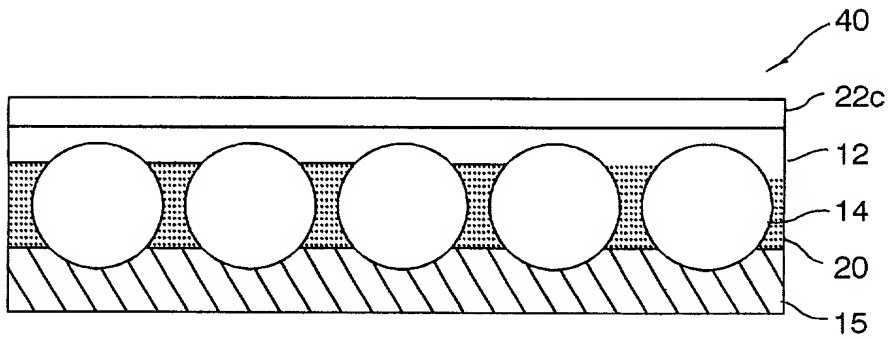
STEP I



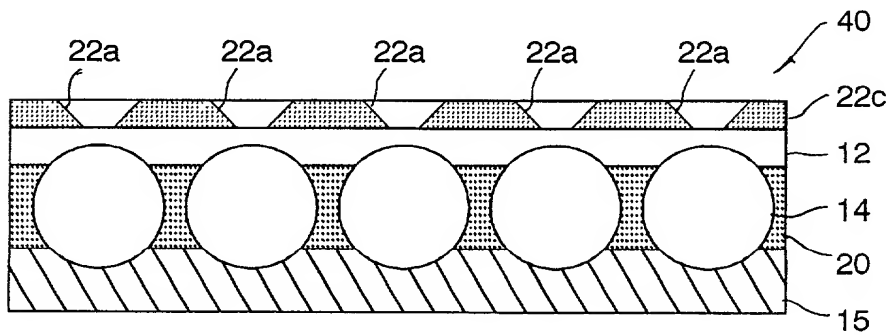
STEP II



STEP III



STEP IV



# FIG. 10

